DC COMPONENTS CO., LTD.

RECTIFIER SPECIALISTS

RL101F THRU RL107F

TECHNICAL SPECIFICATIONS OF FAST RECOVERY RECTIFIER

VOLTAGE RANGE - 50 to 1000 Volts

FEATURES

- * Fast switching
- * Low leakage
- * Low forward voltage drop

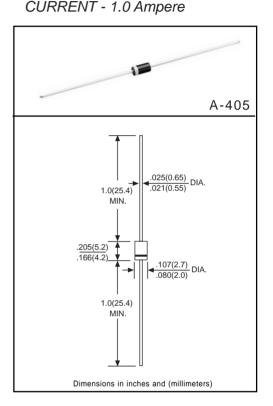
R

- * High current capability
- * High current surge
- * High reliability

MECHANICAL DATA

- * Case: Molded plastic
- * Epoxy: UL 94V-0 rated flame retardant
- * Lead: MIL-STD-202E, Method 208 guaranteed
- * Polarity: Color band denotes cathode end
- * Mounting position: Any
- * Weight: 0.33 gram approx.

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS Rating at 25°C ambient tempature unless ohterwise specified Single phase, half wave 60 HZ, resistive or inductive load. For capacitive load, derate current by 20%.

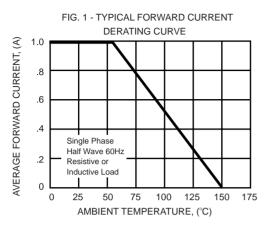


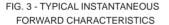
	SYMBOL	RL101F	RL102F	RL103F	RL104F	RL105F	RL106F	RL107F	UNITS
Maximum Recurrent Peak Reverse Voltage	Vrrm	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	Vrms	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TA = 55°C	lo	1.0							Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	30							Amps
Maximum Instantaneous Forward Voltage at 1.0A DC	VF	1.3							Volts
Maximum DC Reverse Current at Rated DC Blocking Voltage TA=25°C	5.0								– μAmps
Maximum Full Load Reverse Current Average, Full Cycle .375"(9.5mm) lead length at T L = 55°C	IK IK	500							
Maximum Reverse Recovery Time (Note 1)	trr		1	50		250	50	00	nSec
Typical Junction Capacitance (Note 2)	CJ	15							pF
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150							°C

Note: 1. Test Conditions: IF = 0.5A, IR = 1.0A, IRR = 0.25A

2. Measured at 1 MHz and applied reverse voltage of 4.0 volts

RATING AND CHARACTERISTIC CURVES (RL101F THRU RL107F)





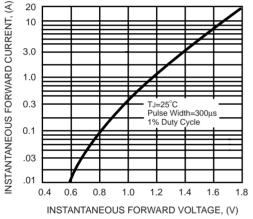
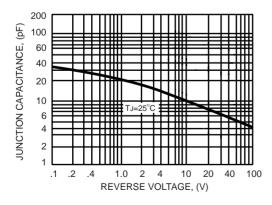


FIG. 5 - TYPICAL JUNCTION CAPACITANCE



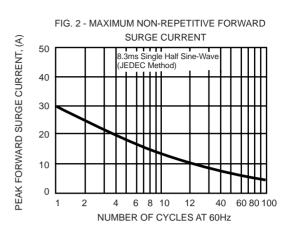


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

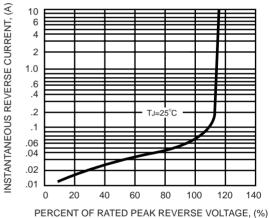
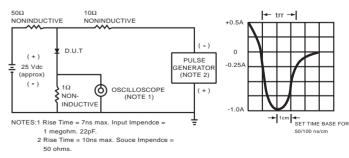


FIG. 6 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARAC TERISTIC



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