

DC COMPONENTS CO., LTD.

RECTIFIER SPECIALISTS

FSM101 THRU FSM107

TECHNICAL SPECIFICATIONS OF FAST RECOVERY RECTIFIER

VOLTAGE RANGE - 50 to 1000 Volts

CURRENT - 1.0 Ampere

FEATURES

- *Ideal for surface mounted applications
- * Low leakage current
- * Fast Switching for high efficiency
- * Glass passivated junction

MECHANICAL DATA

* Case: Molded plastic

* Epoxy: UL 94V-0 rated flame retardant

* Lead: MIL-STD-202E, Method 208 guaranteed

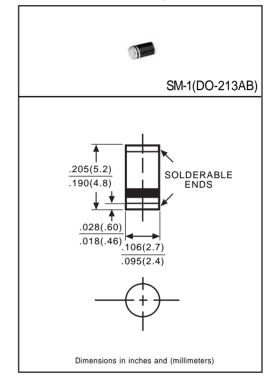
* Polarity: Color band denotes cathode end

* Mounting position: Any * Weight: 0.12 gram approx.

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.



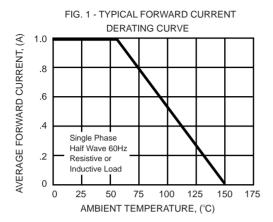
		SYMBOL	FSM101	FSM102	FSM103	FSM104	FSM105	FSM106	FSM107	UNITS
Maximum Recurrent Peak Reverse Voltage		VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage		VRMS	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage		VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TA = 55°C		lo	1.0							Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)		IFSM	30						Amps	
Maximum Instantaneous Forward Voltage at 1.0A DC		VF	1.3						Volts	
Maximum DC Reverse Current at	@TA=25°C	l _R	5.0							μAmps
Rated DC Blocking Voltage	@TA=125°C		100							
Maximum Reverse Recovery Time (Note 3)		trr		150		250	5	00	nSec	
Maximum Thermal Resistance (Note 2)		RθJL	30						°C/W	
Typical Junction Capacitance (Note 1)		CJ	15						pF	
Operating and Storage Temperature Range		TJ, TSTG		-55 to +150						٥C

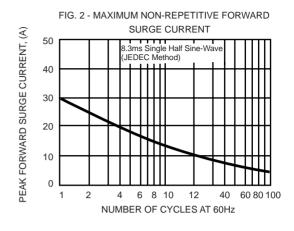
NOTES: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0VDC

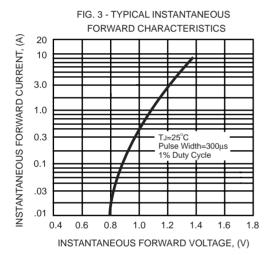
- 2. Thermal Resistance (Junction to Ambient), .24in² (6.0mm²) copper pads to each terminal.
- 3. Test Conditions: IF = 0.5A, IR = 1.0A, IRR = 0.25A

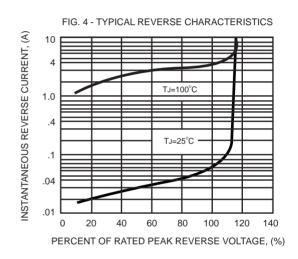
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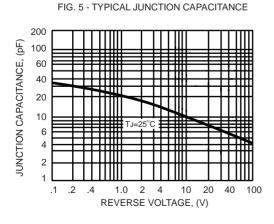
RATING AND CHARACTERISTIC CURVES (FSM101 THRU FSM107)











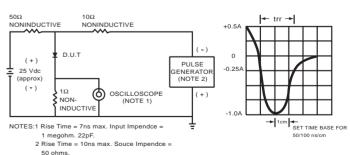


FIG. 6 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARAC TERISTIC

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