

## DC COMPONENTS CO., LTD.

#### RECTIFIER SPECIALISTS

FR301 THRU FR307

# TECHNICAL SPECIFICATIONS OF FAST RECOVERY RECTIFIER VOLTAGE RANGE - 50 to 1000 Volts CURRENT - 3.0 Amperes

#### **FEATURES**

- \* Fast switching
- \* Low leakage
- \* Low forward voltage drop
- \* High current capability
- \* High current surge
- \* High reliability

#### MECHANICAL DATA

\* Case: Molded plastic

\* Epoxy: UL 94V-0 rated flame retardant

For capacitive load, derate current by 20%.

\* Lead: MIL-STD-202E, Method 208 guaranteed

\* Mounting position: Any \* Weight: 1.18 gram approx.

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS Rating at 25°C ambient tempature unless ohterwise specified Single phase, half wave 60 HZ, resistive or inductive load.

DO-27

1.0 (25.4)
MIN.

375 (9.5)
335 (8.5)

1.0 (25.4)
MIN.

220 (5.6)
197 (5.0)
DIA.

Dimensions in inches and (millimeters)

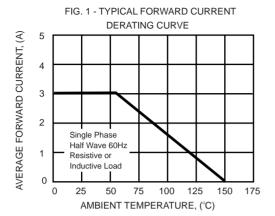
	SYMBOL	FR301	FR302	FR303	FR304	FR305	FR306	FR307	UNITS
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	VRMS	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TA = 55°C	lo	3.0							Amps
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	200							Amps
Maximum Instantaneous Forward Voltage at 3.0A DC	VF	1.3							Volts
Maximum DC Reverse Current at Rated DC Blocking Voltage TA=25°C	10								
Maximum Full Load Reverse Current Average, Full Cycle .375"(9.5mm) lead length at T L = 55°C	I IR				150				μAmps
Maximum Reverse Recovery Time (Note 1)	trr		15	50		250	5	00	nSec
Typical Junction Capacitance (Note 2)	Cı	65							pF
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150							٥C

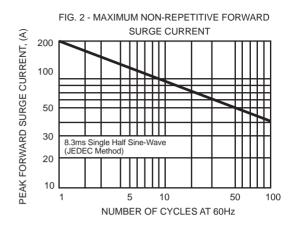
NOTES: 1. Test Conditions: IF = 0.5A, IR = 1.0A, IRR = 0.25A

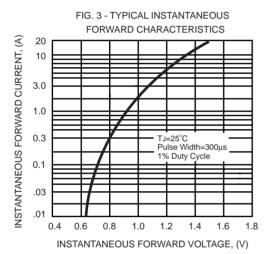
2. Measured at 1MHz and applied reverse voltage of 4.0 volts

REV-3,MAR,2017 1 www.dccomponents.com

### **RATING AND CHARACTERISTIC CURVES (FR301 THRU FR307)**







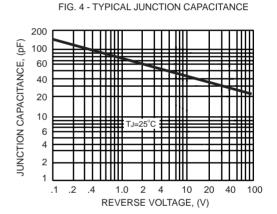
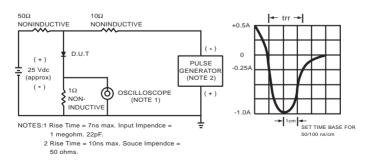


FIG. 5 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARAC TERISTIC



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