

DC COMPONENTS CO., LTD.

RECTIFIER SPECIALISTS

ER2A THRU ER2J

TECHNICAL SPECIFICATIONS OF SURFACE MOUNT SUPER FAST RECTIFIER VOLTAGE RANGE 50 to 600 Volts CURRENT 2.0 Amperes

FEATURES

- * Ideal for surface mounted applications
- * Low leakage current
- * Glass passivated junction

MECHANICAL DATA

* Case: Molded plastic

* Epoxy: UL 94V-0 rate flame retardant *Terminals: Solder plated, solderable per

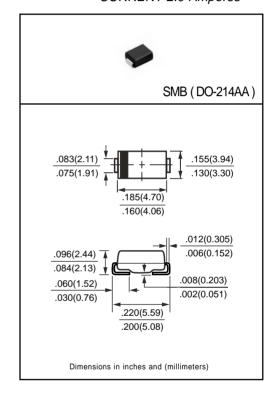
MIL-STD-750, Method 2026

* Polarity: As marked * Mounting position: Any * Weight: 0.093 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%.



		SYMBOL	ER2A	ER2B	ER2C	ER2D	ER2E	ER2G	ER2J	UNITS
Maximum Recurrent Peak Reverse Voltage		Vrrm	50	100	150	200	300	400	600	Volts
Maximum RMS Voltage		VRMS	35	70	105	140	210	280	420	Volts
Maximum DC Blocking Voltage		VDC	50	100	150	200	300	400	600	Volts
Maximum Average Forward Rectified Current at TA = 55°C		lo	3.0							Amps
Peak Forward Surge Current IFM(surge): 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)		IFSM	100						Amps	
Maximum Forward Voltage at 2.0A DC		VF		0.95 1.25			1.7	Volts		
Maximum DC Reverse Current at Rated DC Blocking Voltage	@Ta = 25°C	lo.	5.0							μAmps
	@T _A = 100°C	lr.	200							
Maximum Reverse Recovery Time (Note 1)		trr	35							nSec
Typical Junction Capacitance (Note 2)		Cı	60							pF
Operating and Storage Temperature Range		TJ, TSTG	-65 to +175							٥C

NOTES: 1. Test Conditions: IF=0.5A, IR=1.0A, IRR=0.25A.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0 volrs.

RATING AND CHARACTERISTIC CURVES (ER2A THRU ER2J)

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

(V) 2.5

LINE 2.0

Single Phase Half Wave 60Hz Resistive or Inductive Load

75 100

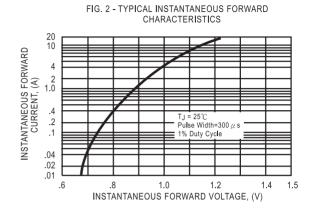
AMBIENT TEMPERATURE, (°C)

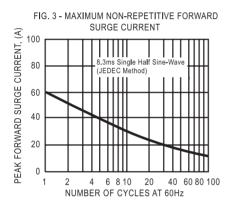
125 150

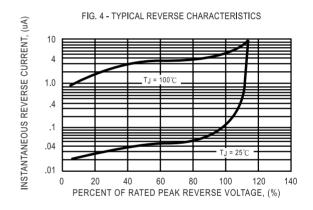
175

0

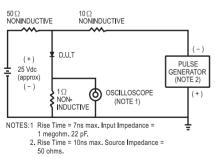
25 50

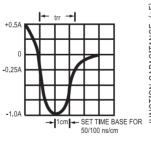












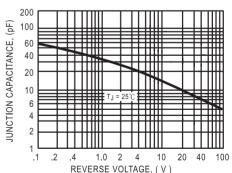


FIG. 6 - TYPICAL JUNCTION CAPACITANCE

